

San José State University
Electrical Engineering Department
CURRICULUM VITAE

NAME

Lili He

ACADEMIC RANK

Professor

DATE OF ORIGINAL APPOINTMENT

August, 1999

YEARS OF SERVICE (as of Fall 2010)

11

YEAR OF ADVANCEMENT IN RANK

| <u>Rank</u> | <u>Year</u> | <u>Institution and Department</u> |
|---------------------|--------------------|---|
| Assistant Professor | 1993 | Northern Illinois University, EE Department |
| Associate Professor | 1997 | NIU |
| Associate Professor | 1999 | San Jose State University, EE Department |
| Professor | 2005 | SJSU |

TIME COMMITMENTS

Percentage of time committed to the program: 100%
Percentage of time available for research or scholarly activities: 20%

TEACHING RESPONSIBILITIES

Semiconductor Devices and Circuits, VLSI technology, Electromagnetics, Senior design project advising, MSEE project advising

EDUCATION

| <u>Degree</u> | <u>Field</u> | <u>Institution</u> | <u>Year</u> |
|----------------------|---------------------|---------------------------|--------------------|
| PhD | EE | SUNY at Buffalo | 1992 |

OTHER RELATED EXPERIENCE

| <u>Description</u> | <u>Year</u> |
|--|--------------------|
| Faculty Summer Research Fellow, US Airforce Research lab in MA | 1997 |
| Stanford Nanofabrication facility, Summer Research Fellow | 2009 |

CONSULTING AND PATENTS

| <u>Description</u> | <u>Year</u> |
|---------------------------|--------------------|
| None | |

PROFESSIONAL REGISTRATION, LICENSING AND CERTIFICATION

| <u>State</u> | <u>Field</u> |
|---------------------|---------------------|
| None | |

HONORS, GRANTS AND AWARDS

National Science Foundation, CAREER Award, 1996

MEMBERSHIP IN PROFESSIONAL SOCIETIES

IEEE Senior member: Chair, IEEE Santa Clara Valley Education Society 2004-2008; IEEE Education Society, IEEE Electronic Device Society, IEEE Circuit and System Society, IEEE Communication Society

INSTITUTIONAL AND PROFESSIONAL SERVICE (last 5 years)

PROFESSIONAL ACTIVITIES, DEVELOPMENT AND AFFILIATIONS (last 5 years)

SELECTED PUBLICATIONS (last 5 years)

1. Jayesh Patil, Morris Jones, and Lili He, "Clock and Data Recovery for a 6 Gbps SerDes Receiver", 2010 International Conference on Information and Applied Electronics(ICIAE), July 7-9, 2010, Chengdu, China
2. Donald Wagner and Lili He, "An Innovative Solar System with High Efficiency and Low Cost", Photovoltaic Specialist Conference (PVSC10), June 21-26, 2010, Honolulu, HI
3. Abishek Mann, Amit Karalkar, Lili He, and Morris Jones, "Low Power Low Phase Noise CMOS PLL", ISQED 2010, San Jose, CA, March 22-24, 2010
4. Adithya V. Kodati, Koneswara S. Vemuri, Lili He, and Morris Jones, "Implementation of Power Managed Hyper Transport System for Transmission of HD Video", ISQED 09, San Jose, CA, March 16-19, 2009
5. Neehar, Jandhyala, Lili He, and Morris Jones, "CMOS based low cost temperature sensor", ISQED 08, San Jose, CA, March 17-19, 2008
6. Son Tran, Morris Jones, and Lili He, "Phase Aligned Clock Multiplier," International Symposium on Signal, System and Electronics 2007, July 30 to August 2, 2007, Montreal, Canada,
7. Sijie Zheng, Jing Liu, and Lili He, "The Mixed-Signal Design of PLL with CMOS Technology", accepted by International Symposium on Signal, System and Electronics 2007, July 30 to August 2, Montreal, Canada,
8. Garrick Ng, X. Xu, B. Yu, T. D. Nguyen, L. He, and M. Meyyappan, "In₂Se₃ Nanowire: Synthesize, Characterization, and Device Fabrication", presented in NIST Nanotech 2007, Santa Clara, CA, May 20 to May 24, 2007,
9. B. Yuan, Y. Zhang, and L. He, "A 8b 10Ms/s Low Power Pipelined A/D Converter", Proceeding of ISQED07, pp. March 2007
10. Y. Li and L. He, "First-order Continuous-time Sigma-delta Modulator", Proceeding of ISQED07, pp. March 2007
11. S. J. Lee, E. A. Allen, L. He, "A Bottom-Up Approach to Interdisciplinary Engineering Education in Nanotechnology", Materials Research Society Symposium Proceedings, Vol. 931, Paper 0931-KK02-05, April 2006, San Francisco, CA.
12. Biye Wang, L. He, and M. Jones, "A low input, low power dissipation LOW CMOS ADC", Proceeding of International Symposium in Quality Electronic Design (ISQED), pp. 2006
13. B. Phan, L. Linh, P. Nuygen, and L. He, "TCAD Advanced Simulation: 65 nm MOSFET", Proceeding of the Sixth International Conference on Microelectronics and Interfaces (ICMI'06), 2006
14. L. He, "Comparison Study of Novel Process in Semiconductor Devices," Proceeding of the Sixth International Conference on Microelectronics and Interfaces (ICMI'05), pp.102-104, 2005
15. J. Huynh, B. Ngo, M. Pham, and L. He, " Design of a 10-bit TSMC 0.25um CMOS Digital to Analog Converter," Proceeding of International Symposium in Quality Electronic Design (ISQED), pp187-192, 2005